



Attorney's Deposit No. 5649-873

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re: Koh et al.

U.S. Patent No.: 6,767,787

Application Serial No.: 09/891,905

Issued: July 27, 2004

Filed: June 26, 2001

For: *Methods of Forming Integrated Circuits Using Masks to Provide Ion Implantation Shielding to Portions of a Substrate Adjacent to an Isolation Region Therein*

Date: October 13, 2004

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Certificate  
OCT 26 2004  
of Correction

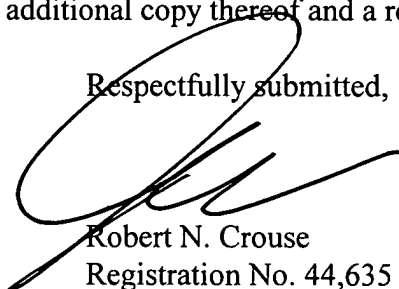
**REQUEST FOR ENTRY OF CERTIFICATE OF CORRECTION UNDER  
35 U.S.C §254 AND 37 C.F.R. §1.322**

Sir:

The Assignee of record for the above-referenced patent hereby requests, pursuant to 35 U.S.C §254 and 37 C.F.R. §1.322, that a Certificate of Correction be issued. This request is made in order to correct the mistakes incurred through the fault of the U.S. Patent and Trademark Office. No fee is believed due. However, the Commissioner is authorized to charge any deficiency or credit any overpayment to Deposit Account No. 50-0220.

The mistakes appearing in the patent are set forth with corrections on the Certificate of Correction enclosed herewith, with an additional copy thereof and a return post card.

Respectfully submitted,

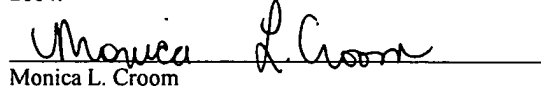


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**Certificate of Mailing under 37 CFR 1.8**

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Monica L. Croom

## UNITED STATES PATENT AND TRADEMARK OFFICE CERTIFICATE OF CORRECTION

PATENT NO. : 6,767,787 *132*  
DATED : July 27, 2004  
INVENTOR(S) : Koh et al.

It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

In the Claims:

Column 6, line 10 should read -- A method of forming a plurality of channel regions between isolation --

Column 6, lines 26-27 should read -- using the mask as an implant mask, thereby forming an implant region in only the first and second portions to --

Column 6, line 29 should read -- forming a plurality of gate electrodes on the implant --

Column 6, lines 34-37 should read -- from the implant region, wherein the separate channel regions are self-aligned to the plurality of gate electrodes. --

Column 6, line 62 should read -- 8. A method of forming a plurality of channel regions between isolation --

Column 7, line 11 should read -- single implant region in only the first and second --

Column 7, line 14 should read -- implant region; and --

Column 7, lines 19-21 should read -- and second spaced apart channel regions from the single implant region. --

Column 8, line 1 should read -- A method of forming a plurality of channel regions between --

Column 8, lines 13-14 should read -- using the mask as an implant mask, thereby forming an implant region in the exposed portion of the substrate --

Column 8, line 16 should read -- forming a plurality of gate electrodes on the implant --

Column 8, lines 22-26 should read -- separate channel regions from the implant region, wherein the separate channel regions are self-aligned to the plurality of gate electrodes. --

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This collection of information is required by 37 CFR 1.322, 1.323, and 1.324. The information is required to obtain or retain a benefit by the public which is to file (and by the USPTO to process) an application. Confidentiality is governed by 35 U.S.C. 122 and 37 CFR 1.14. This collection is estimated to take 1.0 hour to complete, including gathering, preparing, and submitting the completed application form to the USPTO. Time will vary depending upon the individual case. Any comments on the amount of time you are required to complete this form and/or suggestions for reducing this burden, should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, U.S. Department of Commerce, P.O. Box 1450, Alexandria, VA 22313-1450. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Attention Certificate of Corrections Branch, Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.



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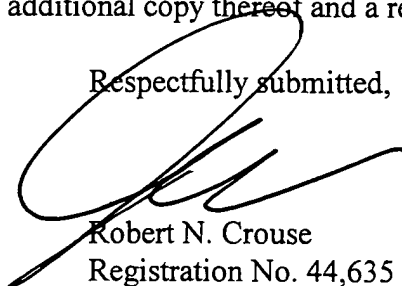
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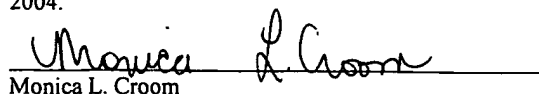
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